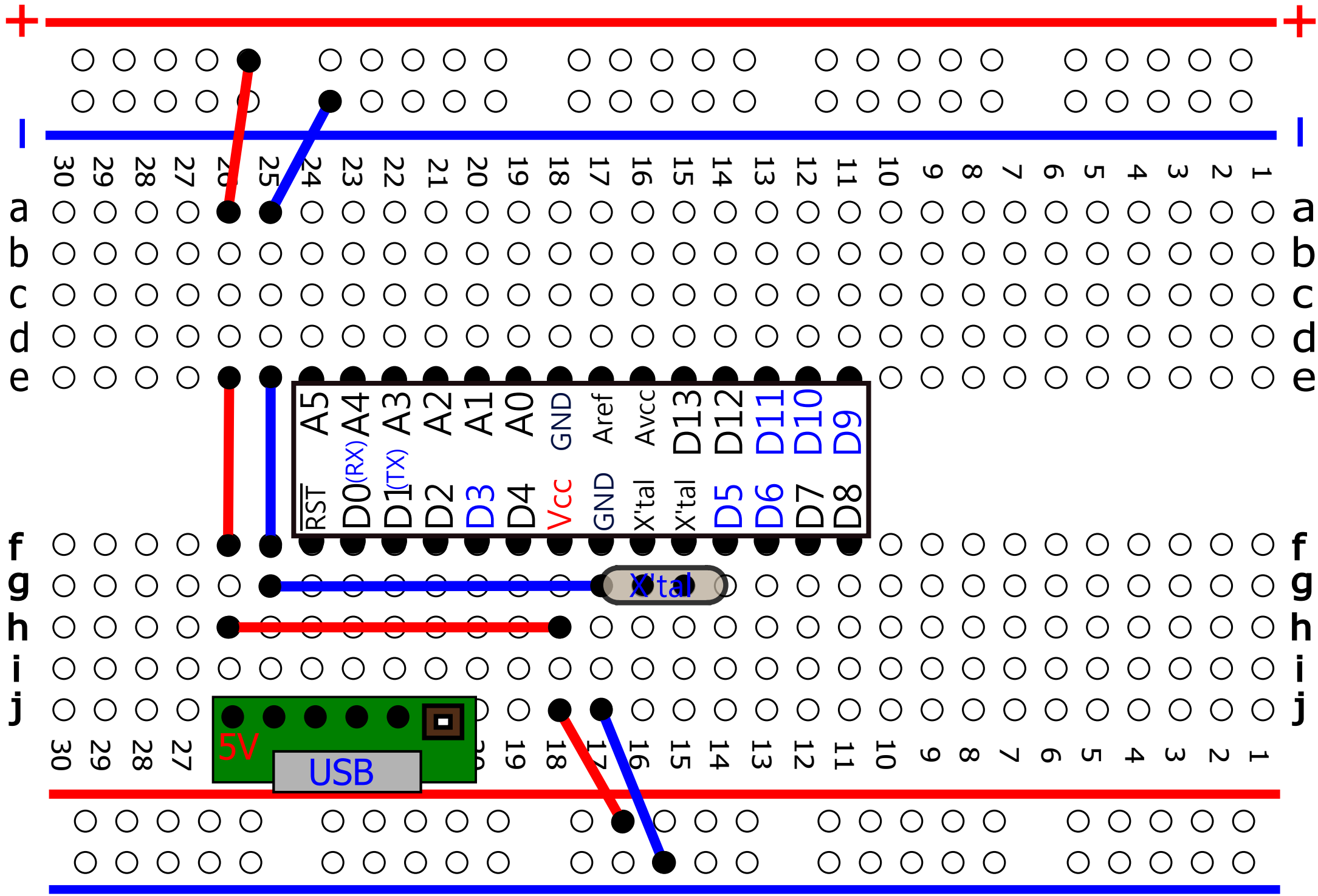

A5
A4
A3
A2
A1
A0
GND
Aref
Avcc
D13
D12
D11
D10
D9
RST
D0(RX)
D1(TX)
D2
D3
D4
Vcc
GND
X'tal
X'tal
D5
D6
D7
D8
X'tal
5V
USB